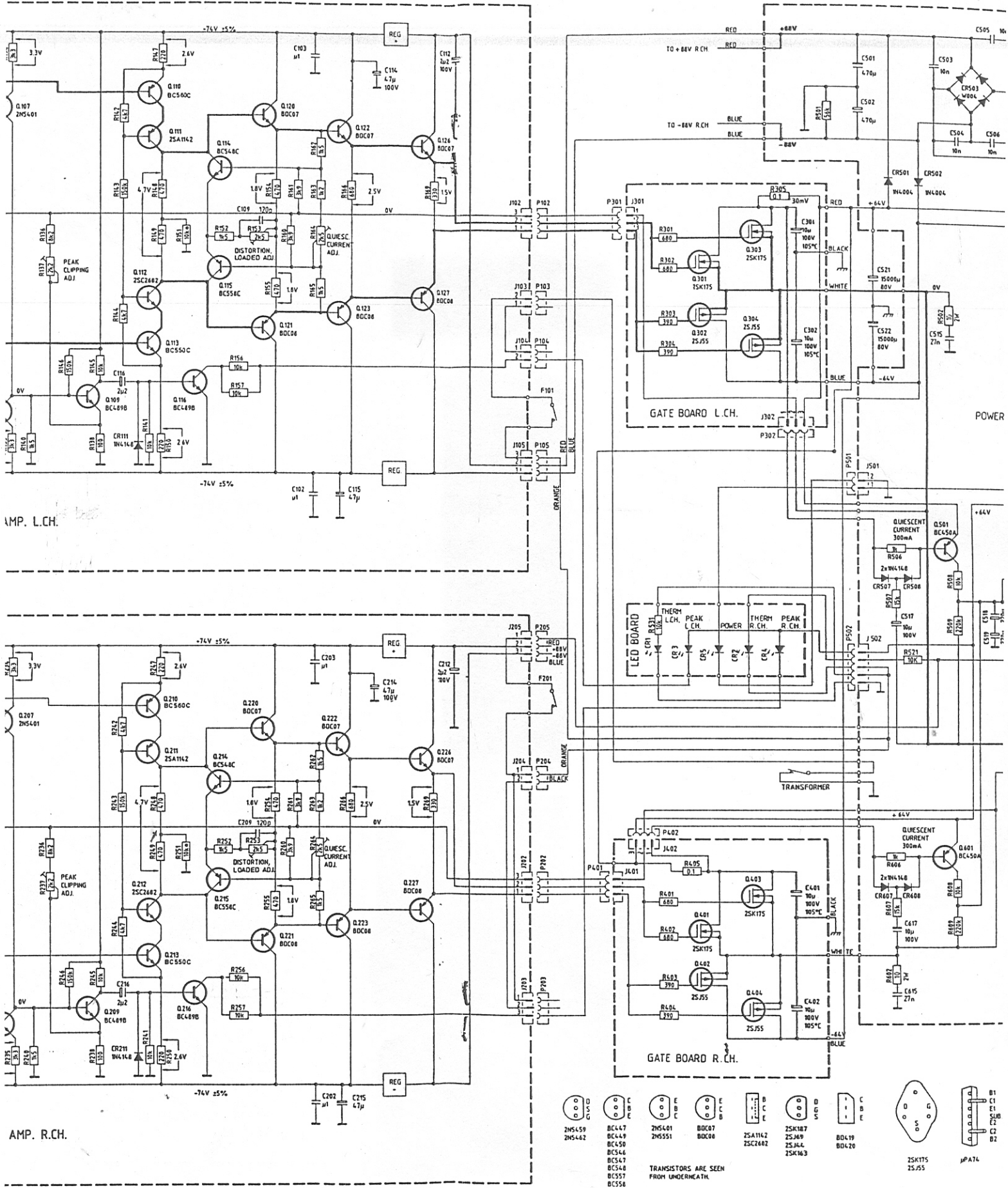


Circuit diagram





AMP. L.CH.

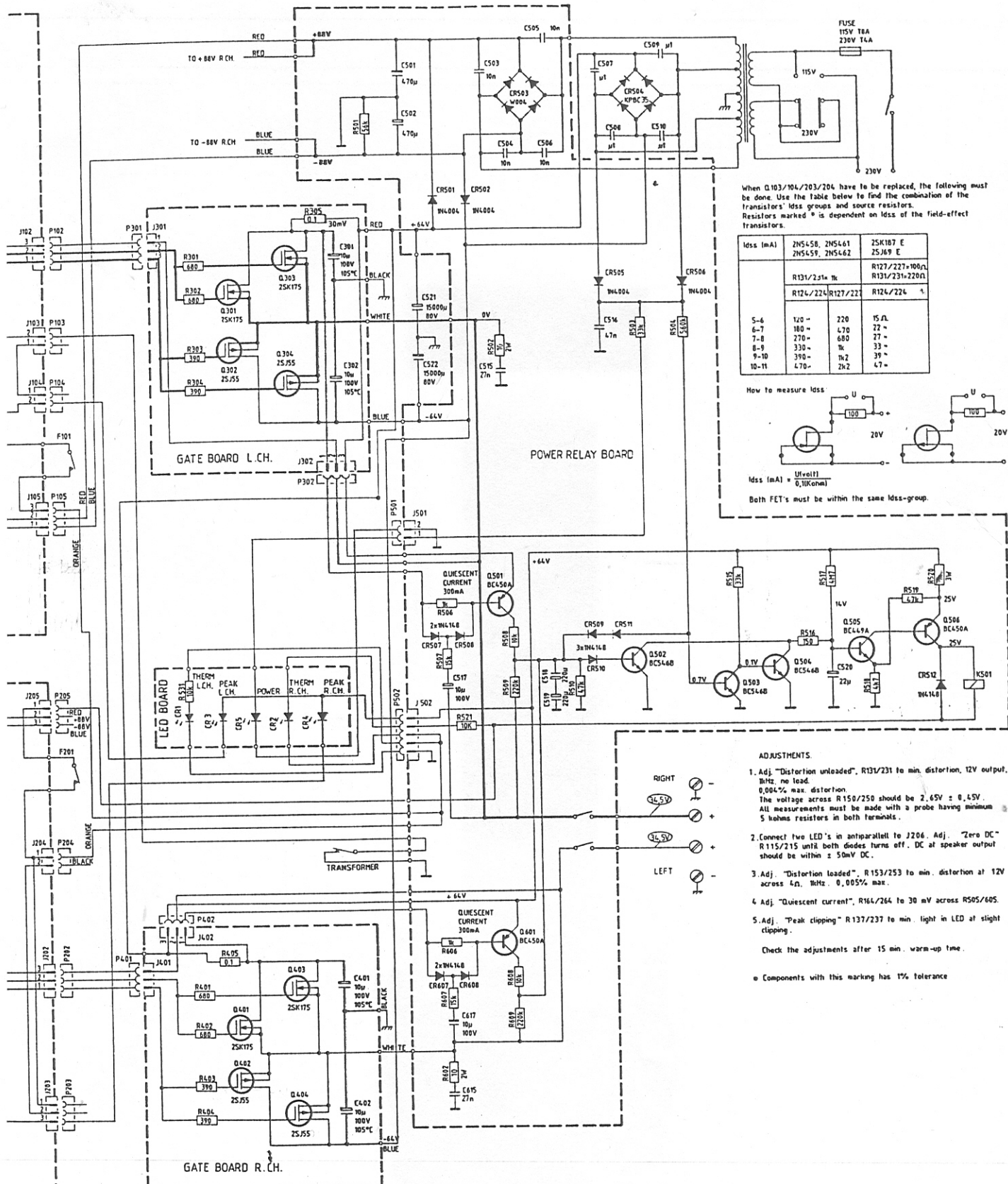
AMP. R.CH.

GATE BOARD L.CH.

GATE BOARD R.CH.

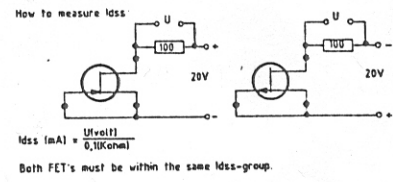
POWER

- |        |       |        |       |         |       |            |             |
|--------|-------|--------|-------|---------|-------|------------|-------------|
| 2N5459 | BC447 | 2N5401 | BDC07 | 2SK187  | BD419 | <br>2SK175 | <br>1/4W474 |
| 2N5462 | BC449 | 2N5551 | BDC08 | 2SK169  | BD420 |            |             |
|        | BC450 |        |       | 2S1162  |       |            |             |
|        | BC546 |        |       | 2SC2462 |       |            |             |
|        | BC547 |        |       |         |       |            |             |
|        | BC548 |        |       |         |       |            |             |
|        | BC557 |        |       |         |       |            |             |
|        | BC558 |        |       |         |       |            |             |
- TRANSISTORS ARE SEEN FROM UNDERNEATH.

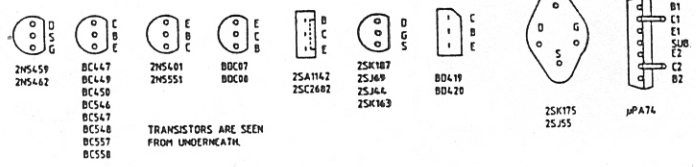


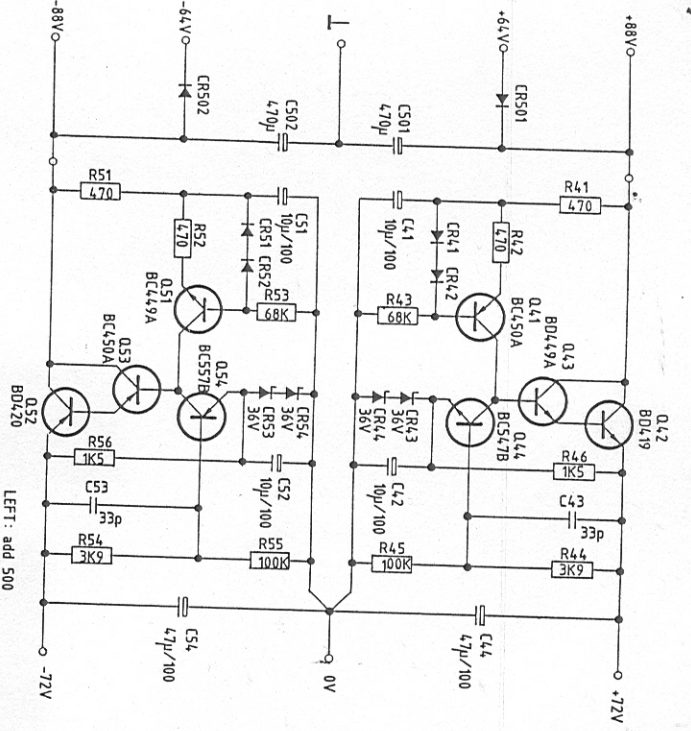
When Q103/104/203/204 have to be replaced, the following must be done. Use the table below to find the combination of the transistors' Idss groups and source resistors. Resistors marked \* is dependent on Idss of the field-effect transistors.

Idss (mA)	2N5458, 2N5461	2N5459, 2N5462	2SK187 E	2SK189 E
		R131/221* 1k	R127/227*100k	R131/231*220k
5-6	120 -	220	15 Ω	22 -
6-7	180 -	470	22 -	27 -
7-8	270 -	680	33 -	39 -
8-9	330 -	1k	47 -	56 -
9-10	390 -	1.2k	68 -	82 -
10-11	470 -	2k2	100 -	150 -

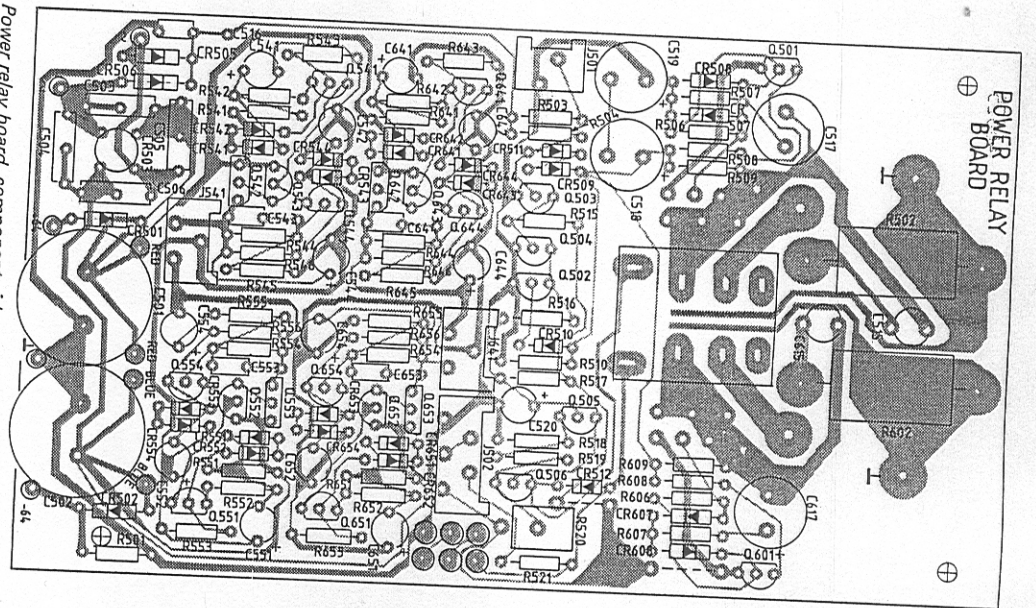
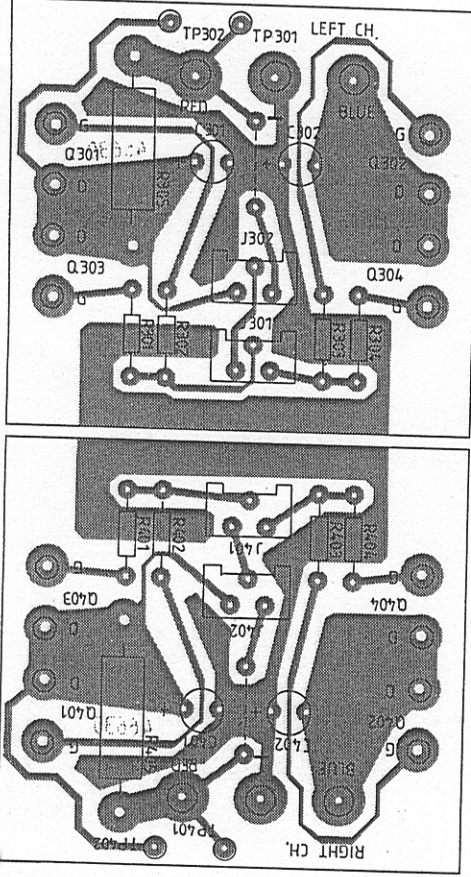


- ### ADJUSTMENTS
- Adj. "Distortion unloaded", R131/231 to min. distortion, 12V output, 0.004% max. distortion. The voltage across R150/250 should be 2.65V ± 0.45V. All measurements must be made with a probe having minimum 5 kohms resistors in both terminals.
  - Connect two LED's in antiparallel to J206. Adj. "Zero DC" R115/215 until both diodes turns off. DC at speaker output should be within ± 50mV DC.
  - Adj. "Distortion loaded", R153/253 to min. distortion at 12V across 4Ω, 10Hz, 0.005% max.
  - Adj. "Quiescent current", R164/264 to 30 mV across R505/605.
  - Adj. "Peak clipping" R137/237 to min. light in LED at slight clipping.
- Check the adjustments after 15 min. warm-up time.
- \* Components with this marking has 1% tolerance

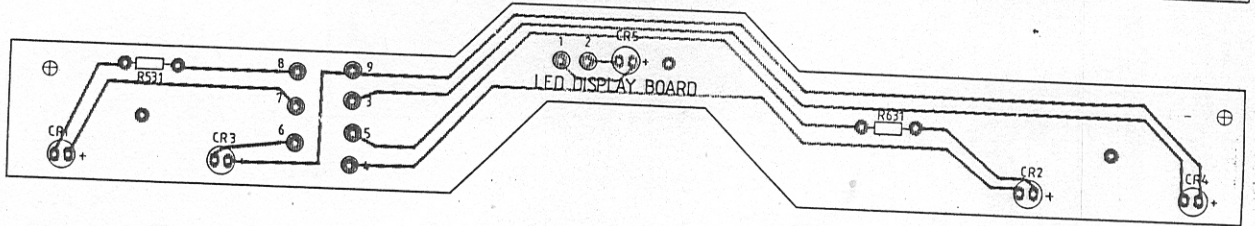




LEFT: add 500  
RIGHT: add 600



Power relay board, component side



LED display board, component side